

Diagram illustrating a TDMA frame structure. The frame is divided into slots (SLOT 1, SLOT 2, SLOT 3, SLOT 4, SLOT 5, SLOT 6, SLOT 7, SLOT 8, ..., SLOT N, ...). Below the slots, the frame is further divided into sections: BCCH, PCCH, ARCH, and ... (indicating further sections). A double-headed arrow at the bottom indicates the duration of the SUPERFRAME.

(Prior Art)

The diagram illustrates the structure of a TDMA frame and its placement within a superframe. At the top, a **TDMA FRAME (40 MS)** is shown, consisting of two **BLOCK (20 ms)** segments. Below this, a table of **SLOT (6.67 MS)** is provided, with slots numbered 1 through 6. Handwritten annotations include '202' pointing to the slot table, '200' pointing to the TDMA frame, and '204', '208', '210', '212', '214', and '216' pointing to specific slots. Below the slot table, a sequence of slots numbered 0 through 31 is shown. Handwritten annotations '204' and '208' also point to slots in this sequence. At the bottom, a **SUPERFRAME (640 MS)** is shown, containing **FBGCH**, **EBGCH**, **SBGCH**, **RESERVED**, and **SPACH** segments. A **BCCH** segment is also indicated below the first three segments.

TDMA FRAME (40 MS)					
BLOCK (20 ms)			BLOCK (20 ms)		
SLOT (6.67 MS) 1	SLOT (6.67 MS) 2	SLOT (6.67 MS) 3	SLOT (6.67 MS) 4	SLOT (6.67 MS) 5	SLOT (6.67 MS) 6

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
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FBGCH | EBGCH | SBGCH | RESERVED | SPACH

BCCH

SUPERFRAME (640 MS)

Figure 2.

(Prior Art)



Figure 3

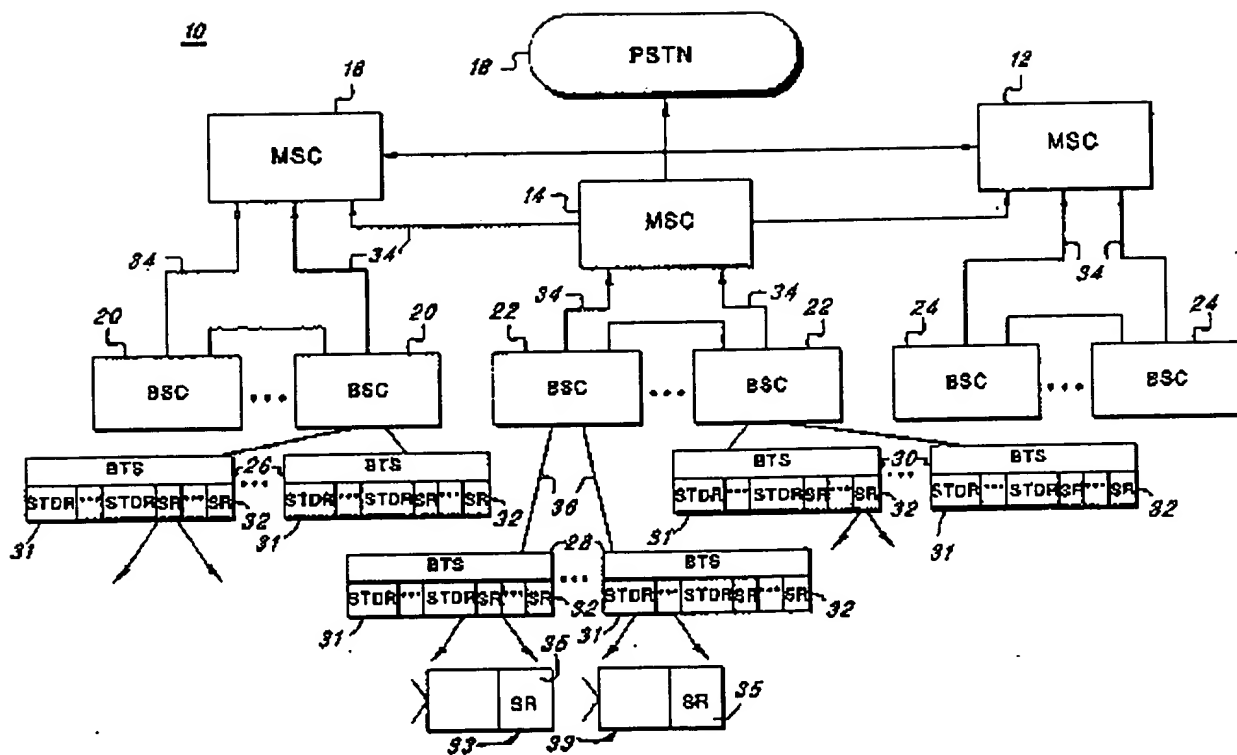


Figure 4

662670 0600260

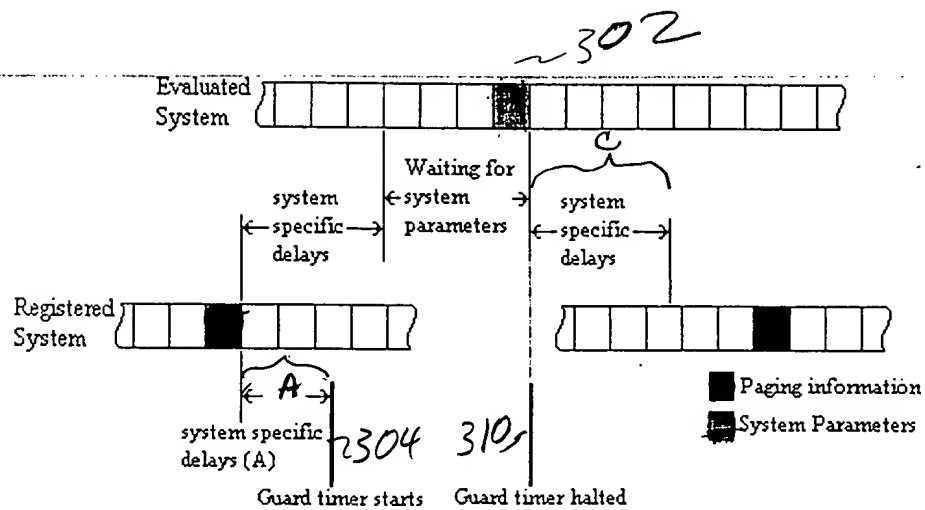


Figure 5

600

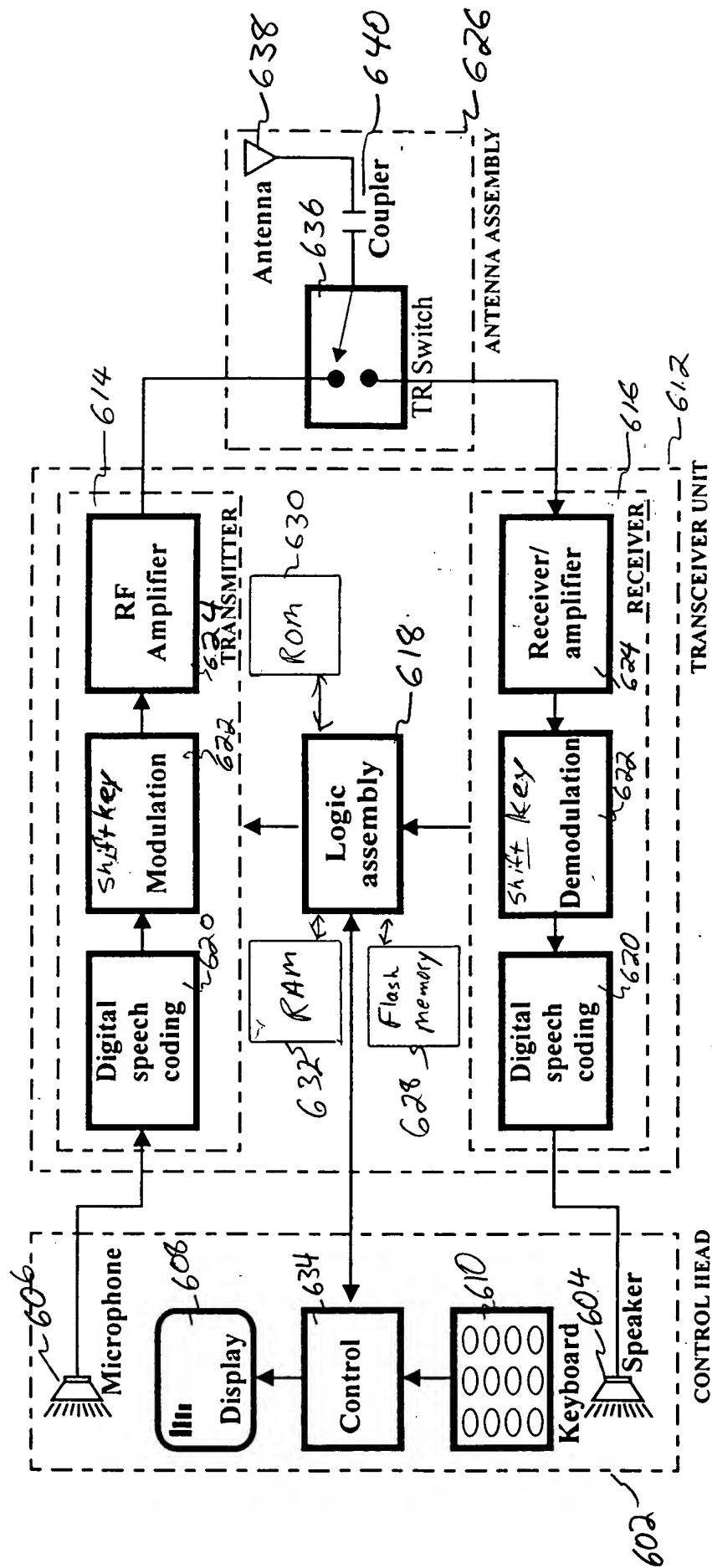


Figure 6